

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Jerrell Hein and Axel Thomsen
 Title: CALIBRATION OF OSCILLATOR DEVICES
 Application No.: 10/675,543 Filed: September 30, 2003
 Examiner: Kinkead, Arnold M. Group Art Unit: 2817
 Atty. Docket No.: 026-0035

October 4, 2004

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INFORMATION DISCLOSURE STATEMENT
37 C.F.R. § 1.97(b)

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- ☒ Form(s) PTO-1449 (6 pages), including copy(ies) of 45 reference(s).
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to the Examiner's attention in the above-identified application. Citation of such information shall not be construed as:

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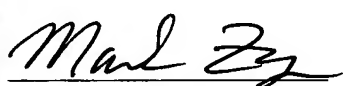
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- ☐ This Information Disclosure Statement is filed within three months of the filing date of a national application other than a continued prosecution application under § 1.53(d) or within three months of entry of the national stage as set forth in § 1.491 in an international application. Therefore, no fee is required.
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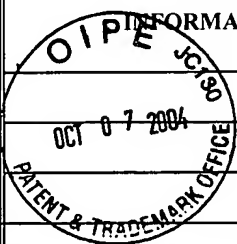
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 Mark Zagorin	<u>10/4/04</u> Date

Respectfully submitted,



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U.S. Department of Commerce, Patent and Trademark Office				Attorney Docket No.: 026-0035		
				Application No.: 10/675,543		
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U.S. Patent Documents						
*Examiner Initial		Document Number	Date	Name		
	AA	3,676,801	Jul. 11, 1972	Musa		
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	AEE	WO 85/ 02966	Jul. 4, 1985	PCT		
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	AII	Fox Electronics, "F6151 / Tri-State HCMOS Dual Programmable Clock Oscillator," 4 pages (from file history of U.S. Patent No. 5,952,890).				
	AJJ	Fox Electronics, "F6053A / Tri-State HCMOS Programmable Clock Oscillator," 4 pages (from file history of U.S. Patent No. 5,952,890).				
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	AXX	Gaskell, Tom, "Semiconductor Circuits Programmable Crystal Oscillator (PX0-600)," <u>Practical Electronics</u> , July 1983, pages 40-43.		
	AYY	Giles, T. G., "A Universal Frequency Synthesizer IC," Philips Telecommunication Review, Vol. 37, No. 3, August 1979, pages 177-181.		
	AZZ	Goto, Junichi, et al., "A Programmable Clock Generator With 50 to 350 MHz Lock Range For Video Signal Processors," <u>IEEE 1993 Custom Integrated Circuits Conference</u> , pages 4.4.1 - 4.4.2.		
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	ABBB	Hybrids International, Ltd., "Hybrids International Product Summary," March 1993, Olathe, Kansas, 7 pages.		
	ACCC	IC Designs, "Satellite Oscillator," Model No. ICD2031A Preliminary Data Sheet, V2.0, May 1991, pages 1-8.		
	ADDD	IC Designs, "Frequency Multiplier," Model No. ICD2032 Preliminary Data Sheet, V2.0, May 1991, pages 1-7.		
	AEEE	IC Designs, "Dual VGA Clock Generator," Model No. ICD2042A, prior to 1995, 1 page.		
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	ASSS	IC Designs, "Dual Programmable ECL/TTL Clock Generator," Model No. ICD2062B, prior to 1995, 1 page.			
	ATTT	IC Designs, "Programmable Graphics Clock Generator," Model No. ICD 2063, prior to 1995, 1 page.			
	AUUU	IC Designs, Model No. ICD 6233, Specifications, February 11, 1994, 32 pages			
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	AXXX	MF Electronics Corp., "Crystal Controlled Oscillators 20 to 220 MHz Programmable ECL," prior to 1995, pages 9, 11, 18-20.			
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	AZZZ	6,008,703	Dec. 28, 1999	Perrott et al.					
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	ABBBB	6,570,453	May 27, 2003	Su et al.					
	ACCCC	5,604,468	Feb. 18, 1997	Gillig					
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					Translation				
		Document	Date	Country	<table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="text-align: center;">Yes</td> <td style="text-align: center;">No</td> </tr> <tr> <td style="height: 20px;"></td> <td style="height: 20px;"></td> </tr> </table>	Yes	No		
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	AEEEE	Wenzel Associates, "Custom Oscillator Configurations," Austin, Texas, 1 page (from file history of U.S. Patent No. 5,952,890)							
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	AHHHH	National Semiconductor, "PLL™ Low Power Frequency Synthesizer for RF Personal Communications," Advance Information, January 1998, 18 pages.							
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	AJJJJ	Ooms, William J., "Improved Frequency Synthesizer," 8182 Motorola Technical Disclosure Bulletin, October 1986, Schaumbourg, ILL, 3 pages.							
	AKKKK	Vectron Laboratories, "Sonet Clock Recovery Module 622.08 MHz," Model No. SCRM-622, 1994, 4 pages.							
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	ALLLL	5,790,614	Aug. 04, 1998	Powell		
	AMMMM	6,404,246	Jun. 11, 2002	Estakhri et al.		
	ANNNN	10/675,529	Sep. 30, 2003	Hein		
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		Document	Date	Country	Yes	No
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	APPPP	Riley, Tom A. D., et al., "Delta-Sigma Modulation in Fractional-N Frequency Synthesis," IEEE Journal of Solid-State Circuits, Vol. 28, No. 5, May 1993, pp. 553-559.				
	AQQQQ	Parmarti, Sudhakar, et al., "A Wideband 2.4-GHz Delta-Sigma Fractional-N PLL With 1-Mb/s In-Loop Modulation," IEEE Journal of Solid-State Circuits, Vol. 39, No. 1, January 2004, pp. 49-62.				
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